

CLAIM AMENDMENTS

1. (Currently Amended) An apparatus for fixing latency of an operation within a deterministic region on a network in a downhole drill string:[]]
said drill string comprising [[]] a plurality of tubulars configured to form an electromagnetic network when coupled to one another to penetrate a subsurface formation;
said tubulars configured with magnetically conductive, electrically insulating elements at both ends thereof for signal passage along each tubular and between coupled tubulars:
said apparatus comprising:
a network interface modem in communication with the network;
a high priority module in communication with the network interface modem;
the high priority module comprising a packet assembler/disassembler, and hardware for performing at least one operation; and
at least one deterministic peripheral device connected to the high priority module;
wherein the fixed latency hardware performs operations on the deterministic peripheral device according to instructions received and interpreted by the packet assembler/disassembler.
2. Canceled
3. (Original) The apparatus of claim 1, wherein the hardware of the high priority module is selected from the group consisting of at least one hardwired circuit, at least one integrated circuit, and at least one FPGA.
4. (Original) The apparatus of claim 1, wherein the packet assembler/disassembler comprises a packet assembler and a packet disassembler wherein the packet assembler and the packet disassembler are two separate circuits.

5. (Original) The apparatus of claim 1, wherein the at least one deterministic peripheral device is selected from the group consisting of a clock, a local clock source, at least one timer, at least one analog circuit, at least one digital circuit and at least one actuator.
6. (Original) The apparatus of claim 5, wherein the clock is a hardware integrated circuit.
7. (Original) The apparatus of claim 5, wherein the local clock source is selected from the group consisting of at least one crystal, at least one transistor, at least one oscillator, at least one RC circuit, at least one LC circuit, and at least one RLC circuit.
8. (Original) The apparatus of claim 5, wherein the clock is synchronized to a GPS clock or a clock source over a LAN.
9. (Original) The apparatus of claim 1, wherein the high priority module is in communication with devices selected from the group consisting of a data buffer, at least one router, at least one node, at least one tool port, at least one data acquisition device.
10. (Original) The apparatus of claim 1, wherein the high priority module is part of the network interface modem.
11. (Original) The apparatus of claim 1, wherein the deterministic region encompasses devices selected from the group consisting of the high priority module, the network interface modem, and the at least one deterministic peripheral device.
12. (Currently Amended) A method for performing an operation within a deterministic region on a network in a downhole drill string, comprising:
disposing a column of tubulars downhole to penetrate a subsurface formation, the
tubulars configured to form an electromagnetic network when coupled to one another;
said tubulars configured with magnetically conductive, electrically insulating elements at both ends thereof for signal passage along each tubular and between coupled tubulars;

providing a high priority module connected to a network interface modem in
communication with the network;
recognizing by the high priority module a packet as the operation; and
performing the operation within the deterministic region and on a peripheral device.

13. Canceled
14. (Original) The method of claim 12, wherein the high priority module fills a field in the packet with data from the peripheral device.
15. (Original) The method of claim 12, wherein the high priority module connected to a buffer.
16. (Original) The method of claim 15, wherein the packet is received from a device selected from the group consisting of a network interface modem, a buffer, a router, a local node, a tool port, and a data acquisition device.
17. (Original) The method of claim 15, wherein the method further comprises the step of forwarding a packet by the high priority module.
18. (Original) The method of claim 17, wherein the packet is forwarded to a device selected from the group consisting of a network interface modem, a buffer, a router, a local node, a tool port, and a data acquisition device.
19. (Original) The method of claim 17, wherein the packet forwarded is selected from the group consisting of the packet and a packet modified by the operation.
20. (Original) The method of claim 12, wherein the operation is a high priority operation which is performed immediately upon recognition.

21. (Currently Amended) An apparatus for fixing latency of an operation within a deterministic region on a downhole network integrated into a drill string;
- said drill string comprising [[:]] a column of tubulars disposed downhole to penetrate a subsurface formation, the tubulars configured to form a downhole electromagnetic network when coupled to one another,
- said tubulars configured with magnetically conductive, electrically insulating elements at both ends thereof for signal passage along each tubular and between coupled tubulars;
- said apparatus comprising:
- a control device near the surface of the downhole drill string, the control device comprises a network interface modem in communication with the downhole network, a high priority module in communication with the network interface modem, and at least one deterministic peripheral device connected to the high priority module.
22. (Original) The apparatus of claim 21, wherein the control device is a computer.
23. (Original) The apparatus of claim 22, wherein the network interface modem and the high priority module are on an insertable computer card.
24. (Original) The apparatus of claim 21, wherein the control device further comprises a connection to a local area network.
25. (Original) The apparatus of claim 21, wherein the at least one deterministic peripheral device is selected from the group consisting of a local clock source, at least one analog circuit, at least one actuator, a clock, and a hardware clock integrated circuit.
26. (Original) The apparatus of claim 25, wherein the local clock source is selected from the group consisting of at least one crystal, at least one transistor, at least one oscillator, at least one RC circuit, at least one LC circuit, and at least one RLC circuit.
27. (Original) The apparatus of claim 25, wherein the clock is synchronized to a GPS clock.

28. (Original) The apparatus of claim 21, wherein the high priority module is part of the network interface modem.
29. (Original) The apparatus of claim 21, wherein the deterministic region encompasses devices on the network selected from the group consisting of transmission media, the high priority module, the network interface modem, and the at least one deterministic peripheral device.
30. (Currently Amended) The apparatus of claim 21, ~~wherein the~~ further comprising a downhole device in communication with the downhole network, said device comprising[[es]] non-deterministic devices selected from the group consisting of a data buffer, at least one router, at least one node, local node circuitry, at least one tool port, and at least one data acquisition device.